

Abstract of the Disclosure

The present invention provides a display device which is capable of fetching input signals of a small amplitude into the 5 inside thereof. A level converting circuit includes a first-conductive-type first transistor having a gate electrode to which input signals are applied through a first capacitive element, a second-conductive-type second transistor having a gate electrode to which input signals are applied through a second 10 capacitive element, a first bias circuit which applies a first bias voltage to the gate electrode of the first transistor and a second bias circuit which applies a second bias voltage to the gate electrode of the second transistor. Here, the first bias voltage is a voltage which turns off the first transistor 15 when a voltage applied to the gate electrode of the first transistor assumes a maximum value and the second bias voltage is a voltage which turns off the second transistor when a voltage applied to the gate electrode of the second transistor assumes a minimum value.